



## TRANSMITTAL OF FORMAL DRAWINGS

Docket No.  
872.0140.U1(US)

In Re Application Of: Ruotsalainen et al.

Serial No.

10/603,893

Filing Date

6/24/2003

Confirmation No.

4891

Examiner

Young, Brian K.

Art Unit

2819

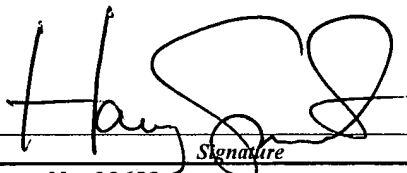
Invention: Switched Voltage-Mode Dither Signal Generation For A Sigma-Delta Modulator

Address to:

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Transmitted herewith are:

3 sheets of formal drawing(s) for this application.

☐ Each sheet of drawing indicates the identifying indicia suggested in 37 CFR Section 1.84(c).  
SignatureCustomer No. 29683  
Harry F. Smith (Reg. No. 32,493)  
Harrington & Smith, LLP  
4 Research Drive  
Shelton, CT 06484-6212

Dated: May 20, 2004

I certify that this document and attached formal drawings  
are being deposited on May 20, 2004 with the  
U.S. Postal Service as first class mail under 37 C.F.R. 1.8  
and addressed to the Commissioner for Patents, P.O. Box  
1450, Alexandria, VA 22313-1450.  
Signature of Person Mailing Correspondence

Elaine F. Mian

Typed or Printed Name of Person Mailing Correspondence